



Architecture Design for Soft Errors

Shubu Mukherjee

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This book provides a comprehensive description of the architectural techniques to tackle the soft error problem. It covers the new methodologies for quantitative analysis of soft errors as well as novel, cost-effective architectural techniques to mitigate them. To provide readers with a better grasp of the broader problem definition and solution space, this book also delves into the physics of soft errors and reviews current circuit and software mitigation techniques.

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Chapter 7: Hardware Error Recovery

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- * Helps readers build-in fault tolerance to the billions of microchips produced each year, all of which are subject to soft errors
- * Shows readers how to quantify their soft error reliability
- * Provides state-of-the-art techniques to protect against soft errors

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